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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10015640	12/10/2001	257	148 ⁺	2814	n. n. G. 0

****APPLICANTS:** Letavic Theodore; Simpson Mark; Abu Lucian; Mukherjee Satyendranath;

****CONTINUING DATA VERIFIED:**

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	US010631
Verified and Acknowledged Examiners's initials <i>h</i>		
TITLE : High frequency high voltage silicon-on-insulator device with mask variable inversion channel and method for forming the same		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
9/10/02		Total Claims 13 Printed Claims for 0.0	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn 2	Figs Drawn Printed Figs
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner Application Examiner	
		PREPARED FOR ISSUE	
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